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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Watts et al.

App. No.: 10/758,384 GPAU: 1722

Filing Date: January 15, 2004 Examiner: Emmanuel S. Luk

Dkt. No.: P78-20-03 Conf. No.: 5448

For: THERMAL PROCESSING SYSTEM FOR IMPRINT LITHOGRAPHY

INFORMATION DISCLOSURE STATEMENT

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The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
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to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

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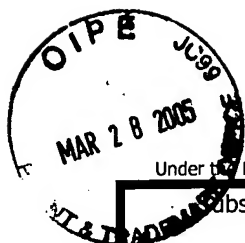
Typed Name: Katrina Prati

Date: 3-24-05

Respectfully Submitted,



Kenneth C. Brooks,
Reg. No. 38,393
Legal Department
Molecular Imprints, Inc.
P.O. Box 81536
Austin, TX 78758-1536
(512) 339-7760
(512) 491-8918 (fax)



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 7

Complete if Known

APPLICATION NUMBER	10/758,384
FILING DATE	01/15/2004
FIRST NAMED INVENTOR	Watts et al.
Group Art Unit	1722
Examiner Name	Luk, Emmanuel S.
Attorney Docket Number	P78-20-03

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	C1	4,731,155		Napoli et al.	3/15/1988	
	C2	5,425,848		Haisma et al.	6/20/1995	
	C3	5,772,905		Chou	6/30/1998	
	C4	6,309,580	B1	Chou	10/30/2001	
	C5	6,334,960	B1	Willson et al.	1/1/2002	
	C6	6,391,217	B2	Schaffer et al.	5/21/2002	
	C7	6,696,220	B2	Bailey et al.	2/24/2004	
	C8	2002/0177319	A1	Chou	11/28/2002	
	C9	6,713,238	B1	Chou et al.	3/30/2004	
	C10	6,646,662	B1	Nebashi et al.	11/11/2003	
	C11	6,776,094	B1	Whitesides et al.	8/17/2004	
	C12	6,518,189	B1	Chou	2/11/2003	
	C13	2002/0132482	A1	Chou	9/19/2002	
	C14	2002/0167117	A1	Chou	11/14/2002	
	C15	6,482,742	B1	Chou	11/19/2002	
	C16	2003/0080471	A1	Chou	5/1/2003	
	C17	2004/0036201	A1	Chou et al.	2/26/2004	
	C18	2004/0192041	A1	Jeong et al.	9/30/2004	

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Sheet 2 of 7

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	C19	6,828,244	B2	Chou	12/7/2004	
	C20	6,809,356	B2	Chou	10/26/2004	
	C21	2004/0197843	A1	Chou et al.	10/7/2004	
	C22	2004/0156108	A1	Chou et al.	8/12/2004	
	C23	2004/0137734	A1	Chou et al.	7/15/2004	
	C24	2004/0131718	A1	Chou et al.	7/8/2004	
	C25	2004/0118809	A1	Chou et al.	6/24/2004	
	C26	2004/0046288	A1	Chou	3/11/2004	
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	C33	2004/0110856	A1	Young et al.	6/10/2004	
	C34	5,028,366		Harakal et al.	7/2/1991	
	C35	5,601,641		Stephens	2/11/1997	
	C36	5,849,209		Kindt-Larsen et al.	12/15/1998	

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Sheet	4	of	7		

FOREIGN PATENT DOCUMENTS							
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		Office 3	Number ⁴ (if known)				

	C38	WO	2004/016406		Sreenivasan	2/26/2004		
	C39	WO	00/21689		Chou et al.	4/20/2000		
	C40	JP	02-92603		Kurikawa et al.	4/3/1990		
	C41	JP	02-24848		Kamio	1/26/1990		
	C42	WO	01/47003		Steiner et al.	6/28/2001		
	C43	WO	02/07199		Chou	1/24/2002		
	C44	WO	2004/114016		Chou et al.	12/29/2004		
	C45	WO	99/05724		Chou et al.	2/4/1999		
	C46	WO	03/010289		Chou et al.	2/6/2003		
	C47	WO	03/079416		Chou	9/25/2003		
	C48	WO	03/099536		Chou et al.	12/4/2003		
	C49	WO	2004/013693		Watts et al.	2/12/2004		

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Sheet	5	of	7		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	C50	Chou, Nanoimprint Lithography and Lithographically Induced Self-Assembly, MRS Bulletin, pp. 512 - 517, 7/1/2001	
	C51	Choi et al., Design of Orientation Stages for Step and Flash Imprint Lithography, Precision Engineering, pp. 192 - 199, 1/1/2001	
	C52	Colburn et al., Step and Flash Imprint Lithography for sub-100 nm Patterning, Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000	
	C53	Chou et al., Imprint Lithography with 25-Nanometer Resolution, Science Vol. 272, pp. 85 - 87, 4/5/1996	
	C54	Chou et al., Imprint Lithography with Sub-10 nm Feature Size and High Throughput, Microelectronic Engineering 35, pp. 237 - 240, 1/1/1997	
	C55	Haisma et al., Mold-assisted Nanolithography: A Process for Reliable Pattern Replication, J. Vac. Sci. Technol. B, pp 4124 - 4128, 11/1/1996	
	C56	Chou et al., Imprint of Sub-25 nm Vias and Trenches in Polymers, Appl. Phys. Lett. 67 (21), 11/20/1995	
	C57	Johnson et al., Advances in Step and Flash Imprint Lithography, SPIE Microlithography Conference, 2/23/2003	
	C58	Chou et al., Lithographically Induced Self-assembly of Periodic Polymer Micropillar Arrays, J. Vac. Sci. Technol. B 17 (6), pp. 3197 - 3202, 11/1/1999	

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Sheet	6	of	7		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	C59	Colburn et al., Step and Flash Imprint Lithography: A New Approach to High-Resolution Patterning, Proc. Of SPIE, vol. 3676, 3/1/1999	
	C60	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	C61	Translation of Japanese Patent 02-92603	
	C62	Translation of Japanese Patent 02-24848	
	C63	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Col. 417, (June 2002), pp. 835-837, 6/1/2002	
	C64	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	
	C65	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	
	C66	Colburn et al., Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers, Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001	
	C67	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	

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	C68	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	C69	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	

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